

FIG. 1
PRIOR ART



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN EACH ADAPTER)

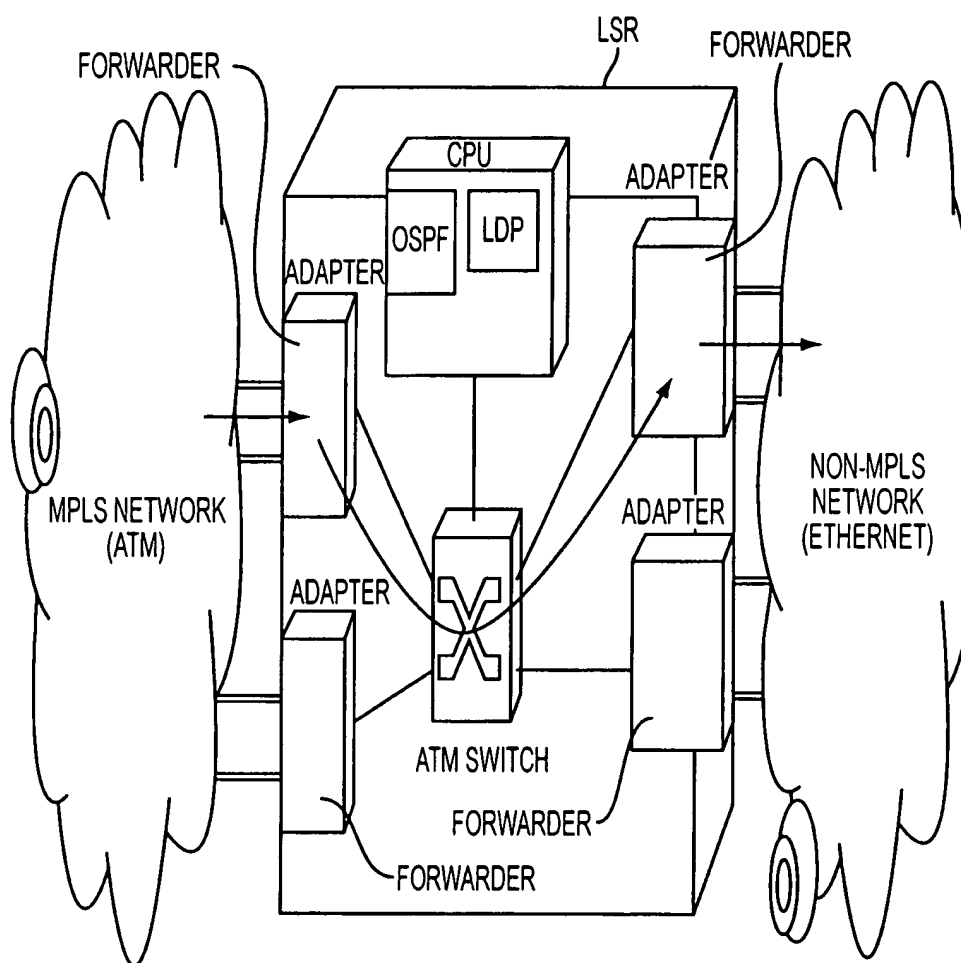


FIG. 3
PRIOR ART



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN ADAPTER
AT EGRESS FOR PACKETS)

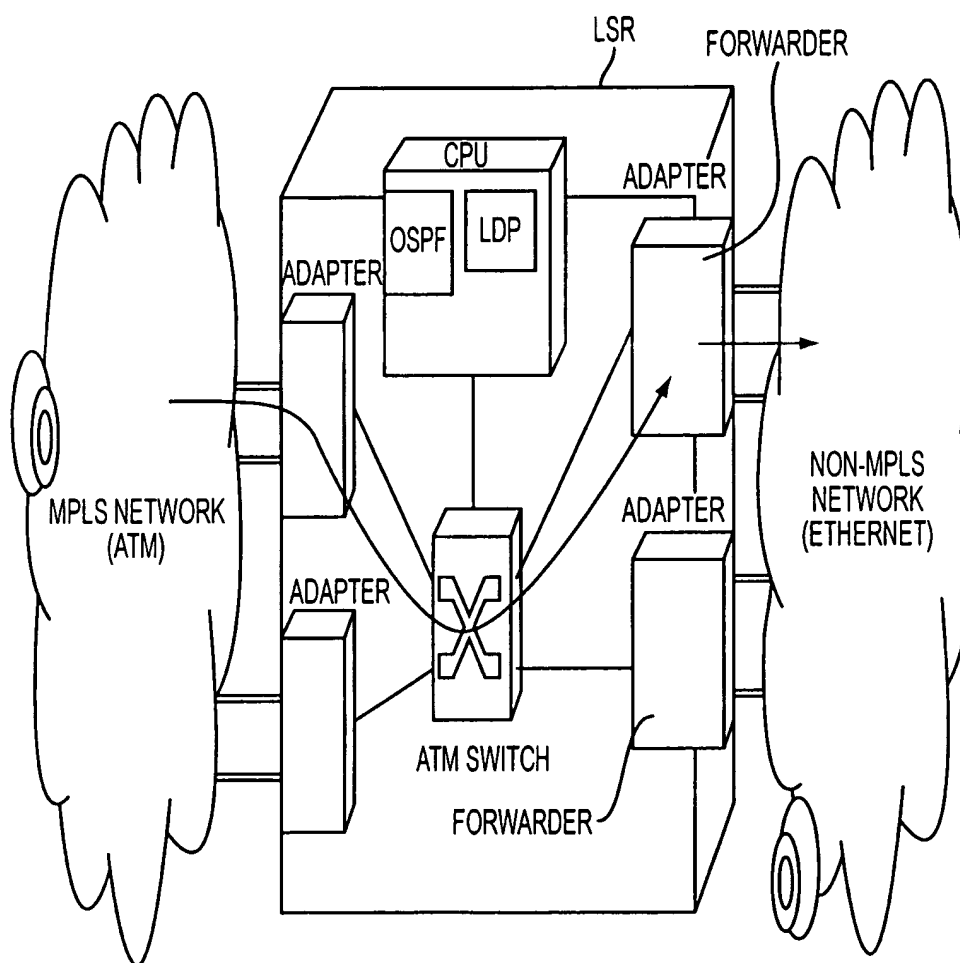


FIG. 4
PRIOR ART

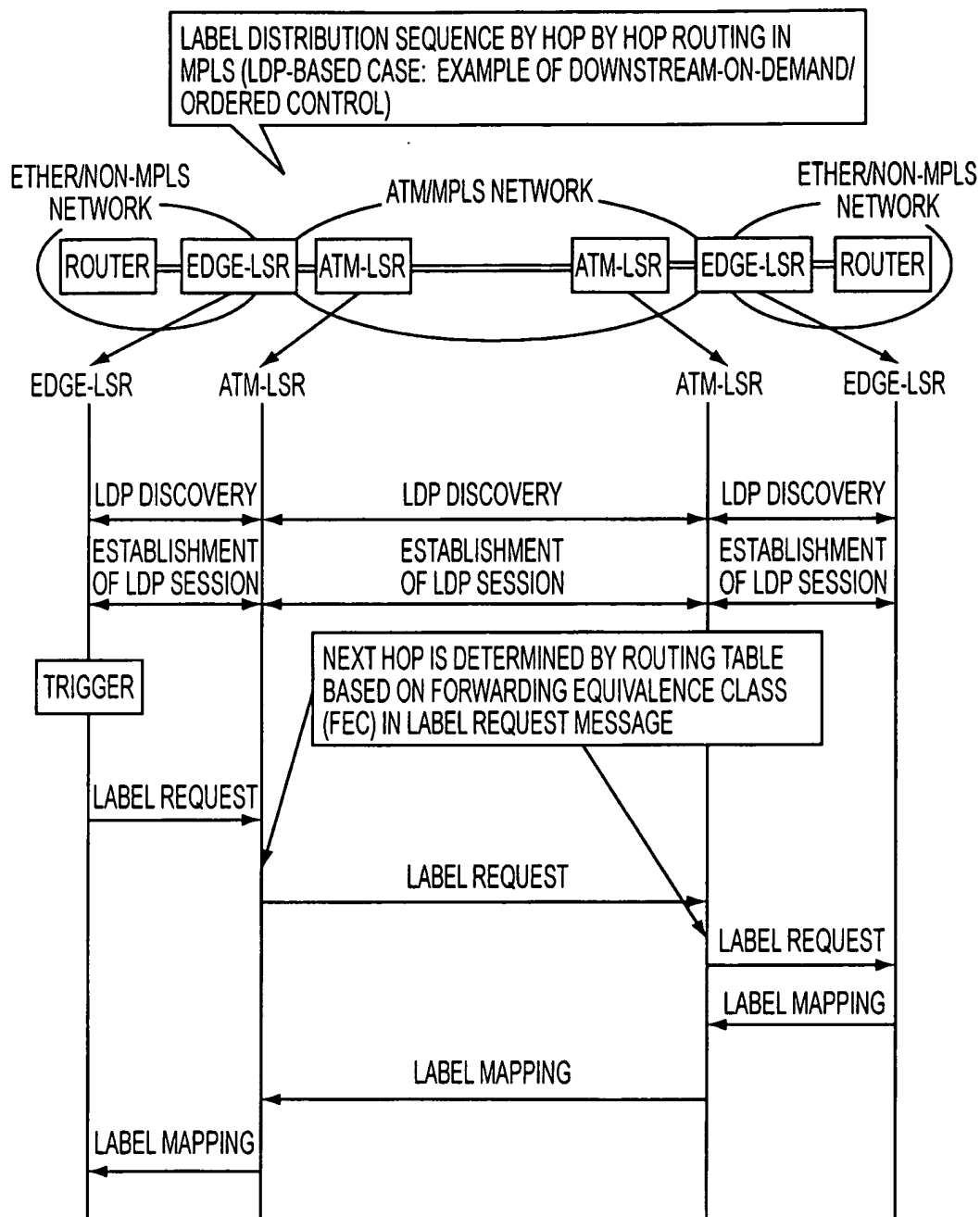


FIG. 5
PRIOR ART

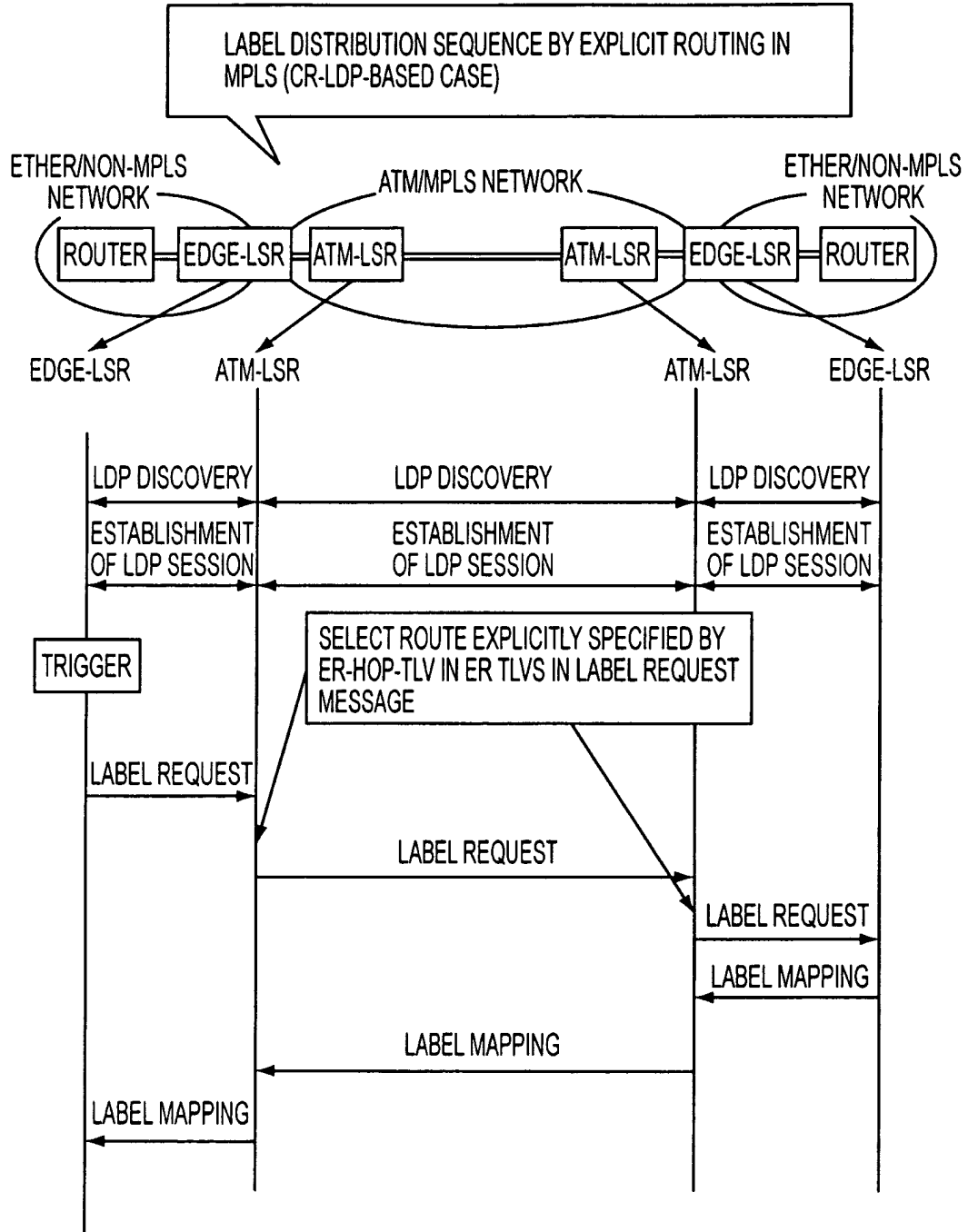


FIG. 6
PRIOR ART

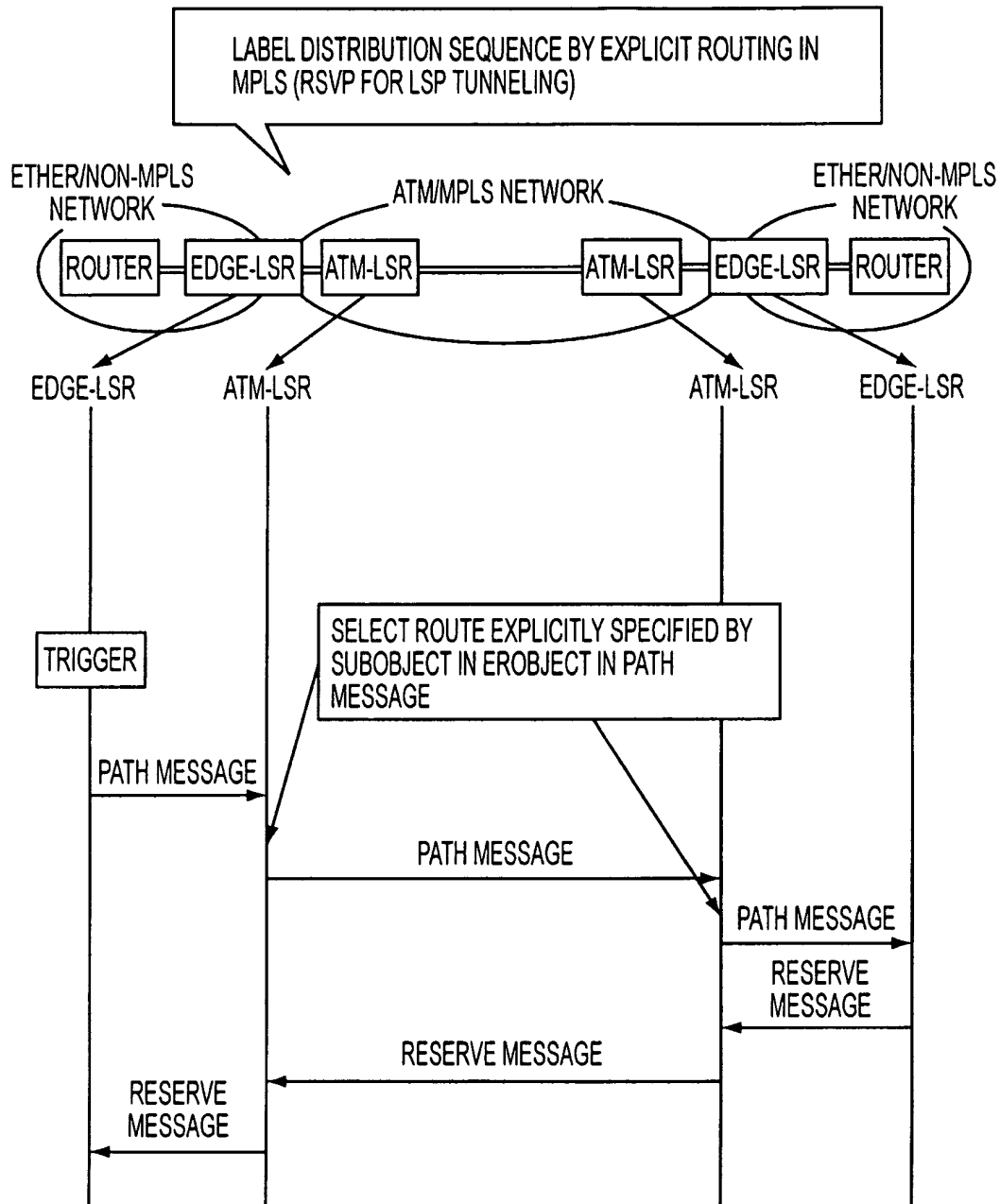
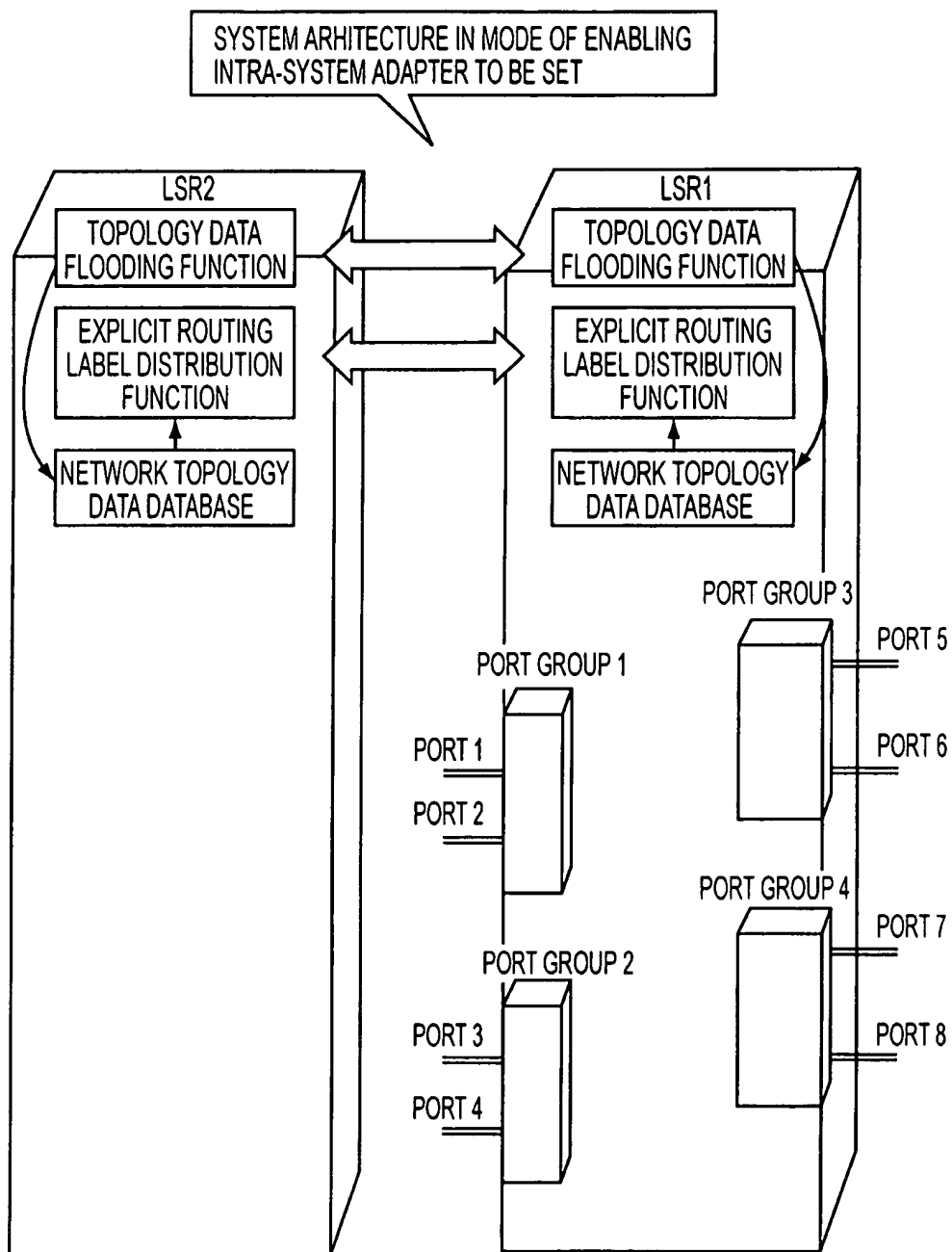
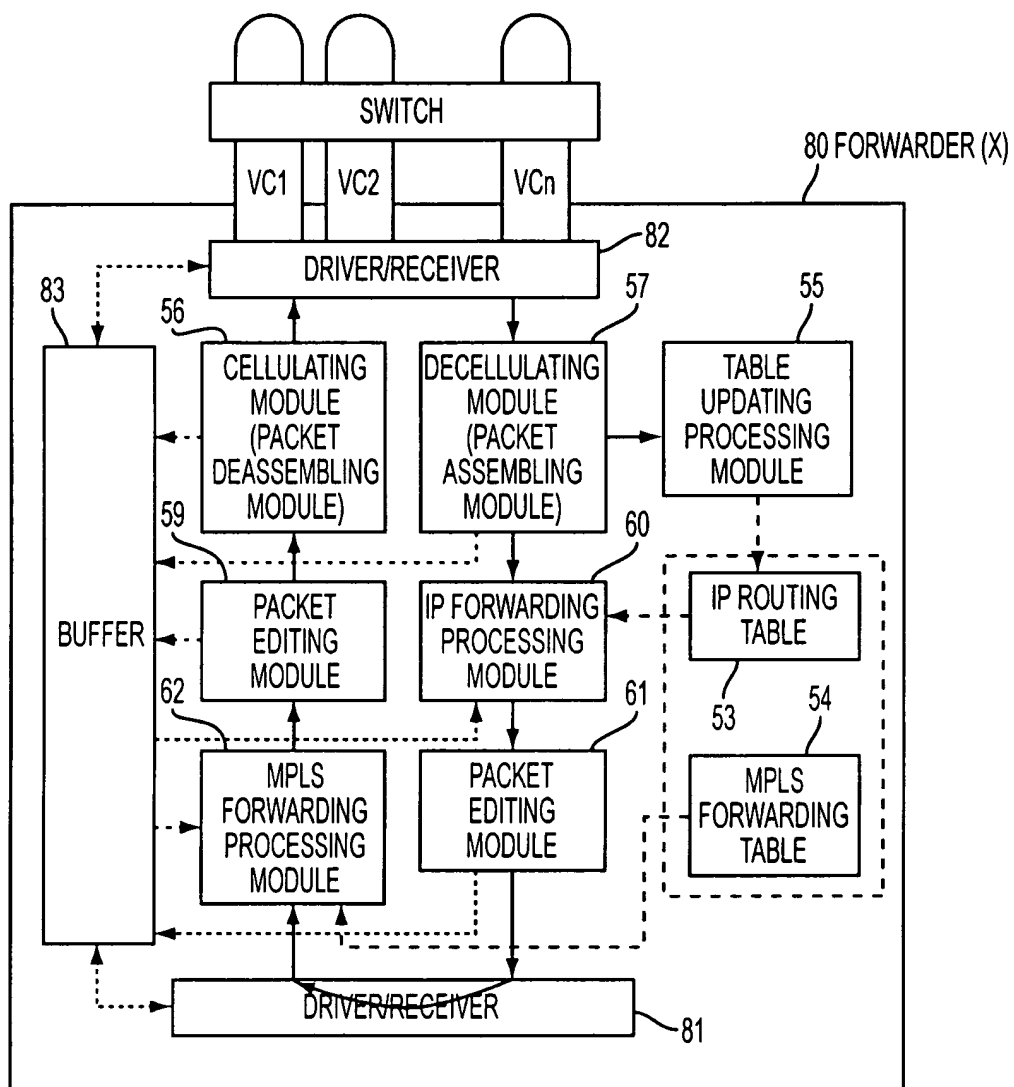


FIG. 7
PRIOR ART





— :CONTROL
 :REFERRING/EDITING OF DATA
 - - - - :REFERRING/UPDATING OF TABLE

FIG. 25